

## Wafer-On-Wafer Room Temperature Permanent Bonding System with Tokyo Institute of Technology WOW Alliance

We, AIMECHATEC, Ltd. (Head Office: Koyodai, Ryugasaki-shi, Ibaraki-pref. President: Isao Abe) are pleased to announce that we have developed Wafer-On-Wafer room temperature permanent bonding system by participating in Tokyo Institute of Technology WOW Alliance. We will contribute to build up mass production process for the most Advanced Semiconductor Packages as new proposal for our Semiconductor Business.

This system will be used in the Wafer-On-Wafer permanent bonding process for the next-generation 3D Stacked Semiconductor production line in Taiwan, which was announced by Tech Extension Co., Ltd., and will be delivered to Wafer-On-Wafer manufacturing process for which production will be started in 2025. We have realized Wafer bonding technology with bonding accuracy of less than 100 nm at room temperature by applying the high-precision bonding technology that we have been cultivating over many years in LCD Panel manufacturing technology.

We are expanding sales of Wafer handling systems including Wafer temporary bonding, support Wafer peeling, and cleaning process, which are important processes for thinner and more multi-layered Wafers. Currently, we are receiving many orders and inquiries from customers due to strong demand and investment in the AI market. We will further expand our Semiconductor Business in the memory market including HBM (High Bandwidth Memory) with our current Wafer handling systems and newly developed Wafer-On-Wafer Room Temperature Permanent Bonding System.

We will continue to contribute to the society through innovation in manufacturing processes by improving lifestyles to be more convenient and affluent.

Your continued kind support to our company is sincerely appreciated.

## References:

Tech Extension Co., Ltd. What's New 2024.4.29
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